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In this webinar, learn why power factor correction (PFC) is needed and how to implement it, followed by an introduction to LLC converters and MPS solutions for both architectures. The following topics will be covered in this webinar:

The power rails in the PL domain are for the RFSoC, and the power rails in the PS domain are for the embedded arm core. The power rails are consolidated to minimize the required number of converters. The MPS power module solution minimizes the PCB space and simplifies the power supply design. Figure 1. MPS Power Solution for Zynq UltraScale+ RFSoC

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